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U.S. **UTILITY** Patent Application

U.S. UTILITY FATERITAPPING	<u></u>
SCANNED ALT Q.A. AA	PATENT DATE

09/751602	CONT/PRIOR	CLASS 711	SUBCLASS	2-1-86	EXAMINER J.
Blaise Fa	nning -				
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i } Method an	d apparati	Hs for :	optimizing.	data stre	aming in a computer stem logic device

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			ISSUIN	G CLAS	SIFIC	ATION	•		
	ORIGIN	AL		CROSS REFERENCE(S)			E(S)		
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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED			
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.		
☐ The term of this patent				NOTICE OF ALLOWANCE MAILED			
subsequent to (date) has been disclaimed.	(Assistant Examiner) (Date)		(Date)	/			
☐ The term of this patent shall							
not extend beyond the expiration date of U.S Patent. No				ISSUE FEE			
				Amount Due	Date Paid ·		
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